

1 4. (Amended) The resistor of claim 1, wherein thickness of said  
2 side sections of said resistor element are twice as thick as said S-shaped section.

1 5. (Amended) The resistor of claim 3, wherein a width of said  
2 side section of said resistor element where the side section extends to said S-shape  
3 section is wider than a width of said S-shaped section.

1 ~~6. (Amended) A method of manufacturing a resistor comprising~~  
2 ~~the steps of;~~

3 ~~forming a pair of electrodes on a substrate; and~~

4 ~~forming a resistor element between said pair of electrodes, said~~  
5 ~~resistor element comprising side sections connected to said electrodes and a S-~~  
6 ~~shaped section disposed between said side sections, said S-shaped section being~~  
7 ~~free of trimmed portion.~~

1 8. (Amended) The method of manufacturing resistor of claim 6,  
2 wherein portion of said side sections is trimmed to adjust a resistance.

Please add the following new claims:

1 9. (Newly Added) The resistor of claim 1, wherein said side  
2 sections are rectangular.

1 10. (Newly Added) The method of manufacturing a resistor of  
2 claim 6, wherein said side sections are rectangular.

1 ~~11. (Newly Added) A resistor comprising:~~  
2 ~~a substrate;~~  
3 ~~a pair of electrodes above said substrate;~~  
4 ~~a resistor element situated between said pair of electrodes, said resistor~~  
5 ~~element including:~~

B2  
83  
6 a pair of side sections, each coupled to a respective one of said  
7 electrodes; and  
8 an S shaped section situated between said pair of side sections;  
9 wherein a width of said s-shaped section is less than a width of each of  
10 said side sections.

1 12. (Newly Added) The resistor of claim 11, wherein each of said  
2 side section includes a respective trimming groove formed therein.

1 13. (Newly Added) The resistor of claim 11, wherein said S-shaped  
2 section is free of trimming grooves.

1 14. (Newly Added) The resistor of claim 12, wherein said S-shaped  
2 section is free of trimming grooves.

Respectfully Submitted,

Lawrence E. Ashery, Reg. No. 34,515  
Attorney for Applicants

LEA/ap/dm  
Dated: October 1, 2001

Suite 301  
One Westlakes, Berwyn  
P.O. Box 980  
Valley Forge, PA 19482-0980  
(610) 407-0700

The Assistant Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. 18-0350 of any fees associated with this communication.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on:

October 1, 2001  
Lawrence E. Ashery